

4V Drive Pch MOSFET

RP1E090RP

Structure

Silicon P-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (MPT6).

Application

Switching

Packaging specifications

Type	Package	Taping	
	Code	TR	
	Basic ordering unit (pieces)	1000	
RP1E090RP		0	

● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	-30	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	Continuous	I_D	±9	Α
	Pulsed	I _{DP} *1	±36	Α
Source current (Body Diode)	Continuous	I _S	-1.6	Α
	Pulsed	I _{SP} *1	-36	Α
Power dissipation		P _D *2	2.0	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

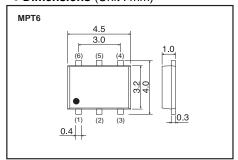
^{*1} Pw≤10µs, Duty cycle≤1%

• Thermal resistance

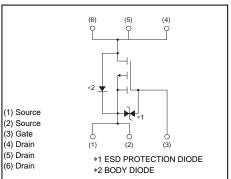
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	62.5	°C/W

^{*}Mounted on a ceramic board.

Dimensions (Unit : mm)



• Inner circuit



^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	1	-	±10	μA	$V_{GS}=\pm20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	-30	1	-	>	$I_D=-1$ mA, $V_{GS}=0$ V
Zero gate voltage drain current	I _{DSS}	1	-	-1	μA	V_{DS} =-30V, V_{GS} =0V
Gate threshold voltage	V _{GS (th)}	-1.0	•	-2.5	V	V_{DS} =-10V, I_{D} =-1mA
Static ducin accuracy as state	*	1	13	16.9		$I_{D} = -9A, V_{GS} = -10V$
Static drain-source on-state resistance	R _{DS (on)}	1	18	25.2	mΩ	$I_D = -9A, V_{GS} = -4.5V$
			21	29.4		$I_D = -9A, V_{GS} = -4.0V$
Forward transfer admittance	IY _{fs} I*	10	-	-	S	$I_D = -9A, V_{DS} = -10V$
Input capacitance	C _{iss}	-	3000	-	pF	V _{DS} =-10V
Output capacitance	C _{oss}	1	360	-	рF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	360	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	20	-	ns	I _D =-4.5A, V _D ; −15V
Rise time	t _r *	1	30	-	ns	V _{GS} =-10V
Turn-off delay time	t _{d(off)} *	1	135	-	ns	$R_L=3.3\Omega$
Fall time	t _f *	1	80	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	1	30	-	nC	I _D =-9A
Gate-source charge	Q _{gs} *	1	7	-	nC	V _{DD} ≒15V
Gate-drain charge	Q _{gd} *	-	11	-	nC	V _{GS} =-5V

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	-1.2	V	I _s =-9A, V _{GS} =0V

^{*}Pulsed

Electrical characteristic curves

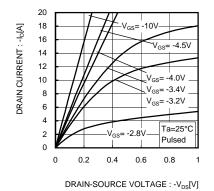


Fig.1 Typical output characteristics(I)

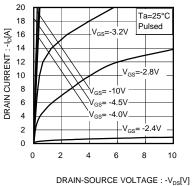
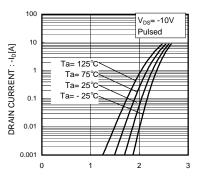


Fig.2 Typical output characteristics(II)



GATE-SOURCE VOLTAGE: -VGS[V]

Fig.3 Typical Transfer Characteristics

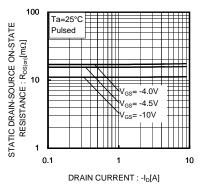


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

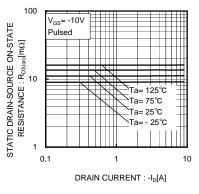


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

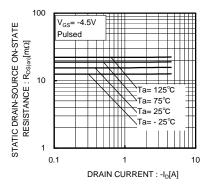
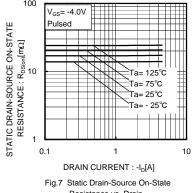


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(III)



Resistance vs. Drain

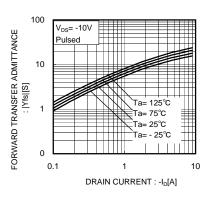


Fig.8 Forward Transfer Admittance vs. Drain Current

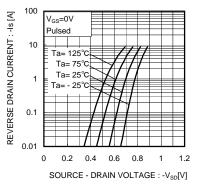


Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage

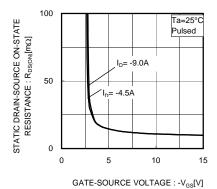


Fig.10 Static Drain-Source On-State Resistance vs. Gate Source Voltage

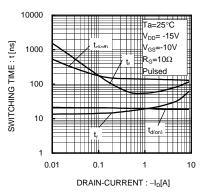
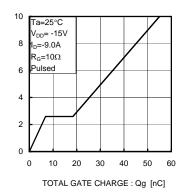


Fig.11 Switching Characteristics



GATE-SOURCE VOLTAGE :-V_{GS} [V]

Fig.12 Dynamic Input Characteristics

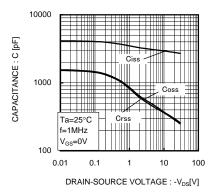


Fig.13 Typical Capacitance vs. Drain-Source Voltage

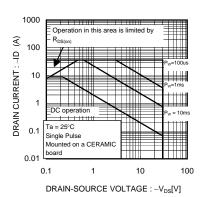


Fig.14 Maximum Safe Operating Aera

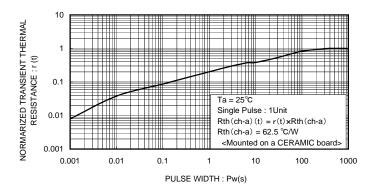


Fig.15 Normalized Transient Thermal Resistance vs. Pulse Width

Measurement circuits

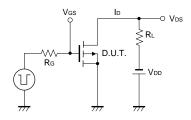


Fig.1-1 Switching Time Measurement Circuit

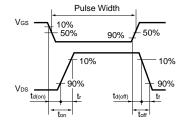


Fig.1-2 Switching Waveforms

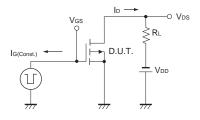


Fig.2-1 Gate charge measurement circuit

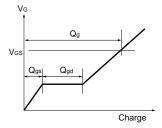


Fig.2-2 Gate Charge Waveform

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