

# ADD-A-PAK Gen 7 Power Modules Schottky Rectifier, 200 A



PRODUCT SUMMARY				
I <sub>F(AV)</sub>	200 A			
$V_{R}$	45 V			
Package	ADD-A-PAK Gen 7			
Circuit	Two diodes common cathode			

#### **MECHANICAL DESCRIPTION**

The ADD-A-PAK Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL pending
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **BENEFITS**

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- Easy mounting on heatsink

#### **ELECTRICAL DESCRIPTION**

The VS-VSKCS200/45 Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	200	Α			
$V_{RRM}$		45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	12 800	Α			
V <sub>F</sub>	100 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.73	V			
TJ	Range	-55 to +150	°C			

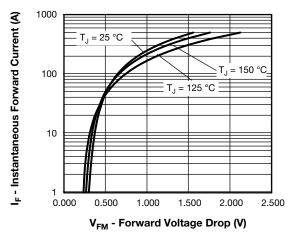
VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-VSKCS200/045	UNITS		
Maximum DC reverse voltage	$V_{R}$	45	V		
Maximum working peak reverse voltage	$V_{RWM}$	45	V		

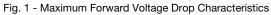


ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T <sub>C</sub> = 91 °C, rectangular waveform		200	
forward current	per leg	I <sub>F(AV)</sub>			100	
Maximum peak one cycle		I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	12 800	Α
non-repetitive surge current			1 3101	rated V <sub>RRM</sub> applied	1700	
Non-repetitive avalanche energ	У	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 19 A, L = 1 mH		180	mJ
Repetitive avalanche current		I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by $T_J$ maxim	•	15	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> -	100 A	T <sub>J</sub> = 25 °C	0.67	V
Maximum forward voltage drop		200 A		0.92	
Maximum forward voltage drop		100 A	T <sub>J</sub> = 125 °C	0.73	ľ
		200 A		1.14	
Maximum rayaraa laakaga aurrant		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mΛ
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C		800	mA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5200	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	)	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation	0.52	°C/W
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>		0.1	C/VV
Approximate weight				75	g
Approximate weight				2.7	oz.
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm
	busbar		spread of the compound.	3	INIII
Case style	·		JEDEC®	TO-240AA co	mpatible





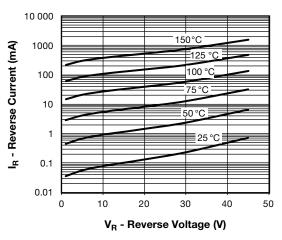


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

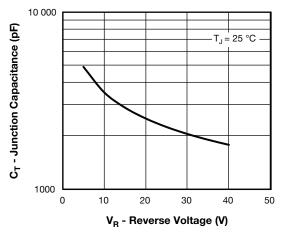


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

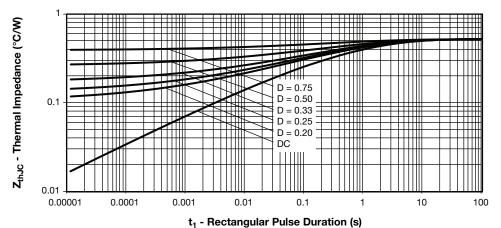


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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# Vishay Semiconductors

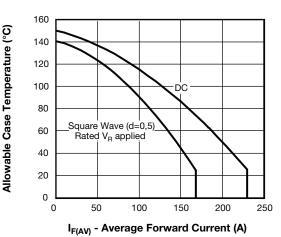
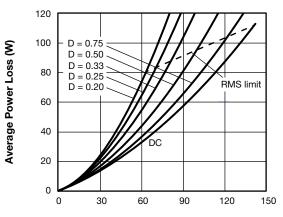


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics

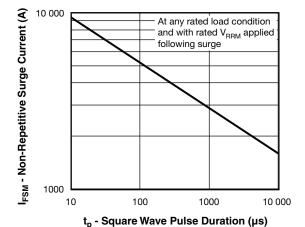


Fig. 7 - Maximum Non-Repetitive Surge Current

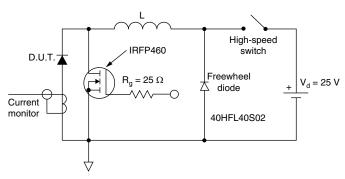


Fig. 8 - Unclamped Inductive Test Circuit

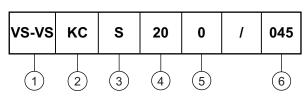
#### Note

Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 



#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

**2** - Circuit configuration:

KC = ADD-A-PAK - 2 diodes/common cathode

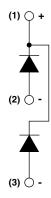
S = Schottky diode

4 - Average current rating (20 = 200 A)

Product silicon identification

Voltage rating (045 = 45 V)

#### **CIRCUIT CONFIGURATION**

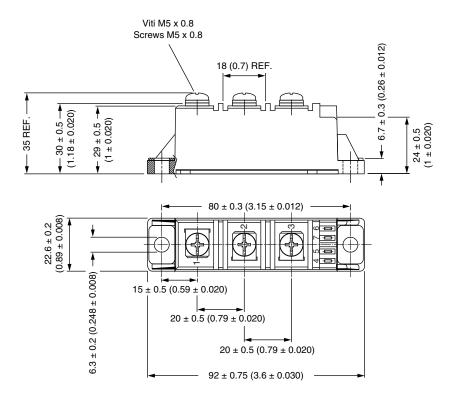


	LINKS TO RELATED DOCUMENTS				
Γ	Dimensions	www.vishay.com/doc?95369			



# **ADD-A-PAK Generation VII - Diode**

#### **DIMENSIONS** in millimeters (inches)





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