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October 2016



# ON Semiconductor® FFSH40120ADN\_F155 Silicon Carbide Schottky Diode 1200 V, 40 A

#### Features

- Max Junction Temperature 175 °C
- Avalanche Rated 200 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery

# Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

## Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.



#### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted. (per leg)

Symbol	Parameter		FFSH40120ADN_F155	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage		1200	V
E <sub>AS</sub>	Single Pulse Avalanche Energy	ngle Pulse Avalanche Energy (Note 1)		mJ
I <sub>F</sub>	Continuous Rectified Forward Current @ T <sub>C</sub>	Continuous Rectified Forward Current @ T <sub>C</sub> < 148 °C		
	Non-Repetitive Peak Forward Surge Current	T <sub>C</sub> = 25 <sup>o</sup> C, 10 μs	1190	А
I <sub>F, Max</sub>		T <sub>C</sub> = 150 <sup>o</sup> C, 10 μs	990	А
I <sub>F,SM</sub>	Non-RepetitiveForwardSurgeCurrent	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	135	А
I <sub>F,RM</sub>	Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	74	А
Ptot	Power Dissipation	T <sub>C</sub> = 25 °C	220	W
FIUI	Power Dissipation	T <sub>C</sub> = 150 <sup>o</sup> C	37	W
T <sub>J</sub> , T <sub>STG</sub>	T <sub>STG</sub> Operating and Storage Temperature Range   TO247 Mounting Torque, M3 Screw		-55 to +175	°C
			60	Ncm

#### **Thermal Characteristics**

Symbol	Parameter	FFSH40120ADN_F155	Unit	
R <sub>0JC</sub> Thermal Resistance, Junction to Case, Max.		0.68* / 0.34**	°C/W	
****				

\* Per Leg, \*\* Per Device

FFSH40120ADN
' <b>п</b>
_F155
; Сі
Silicon
Carbide
Silicon Carbide Schottky Diode
Diode

Package Marking and Ordering Information				
Part Number	Top Mark	Package	Packing Method	Reel Size
		TO-247	Tuba	N1/A

# Long Lead Electrical Characteristics T<sub>C</sub> = 25°C unless otherwise noted. (per leg)

FFSH40120ADN

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 25 <sup>o</sup> C	-	1.45	1.75	
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 20 A, T <sub>C</sub> = 125 <sup>o</sup> C	-	1.7	2	V
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 175 <sup>o</sup> C	-	2	2.4	
		VR = 1200 V, T <sub>C</sub> = 25 °C	-	-	200	
I <sub>R</sub>	Reverse Current	VR = 1200 V, T <sub>C</sub> = 125 <sup>o</sup> C	-	-	300	μA
		VR = 1200 V, T <sub>C</sub> = 175 <sup>o</sup> C	-	-	400	
Q <sub>C</sub>	Total Capacitive Charge	V = 800 V	-	120	-	nC
С		V <sub>R</sub> = 1 V, f = 100 kHz	-	1220	-	
	Total Capacitance	V <sub>R</sub> = 400 V, f = 100 kHz	-	111	-	pF
		V <sub>R</sub> = 800 V, f = 100 kHz	-	88	-	

Tube

Notes: 1: EAS of 200mJ is based on starting  $T_J$  = 25 °C, L = 0.5 mH,  $I_{AS}$  = 29 A, V = 150 V.

FFSH40120ADN\_F155

## Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted (per leg).



**Figure 3. Reverse Characteristics** 





Tape Width

N/A

N/A

Quantity

30 units



## **Figure 4. Current Derating**





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- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 1994
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