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Kind regards,

Team Nexperia

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- Average forward current I_{F(AV)} ≤ 2 A
- Reverse voltage V_R ≤ 60 V
- Low forward voltage V_F ≤ 575 mV
- Low reverse current
- Reduced Printed-Circuit-Board (PCB) area requirements
- Exposed heat sink (cathode pad) for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with visible and solderable side pads
- · Suitable for Automatic Optical Inspection (AOI) of solder joints
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Free-wheeling application
- Reverse polarity protection
- Low power consumption application
- Battery chargers for mobile equipment
- LED backlight for mobile application

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)} average forward current		δ = 0.5; f = 20 kHz; $T_{amb} \le 65$ °C; square wave	[1]	-	-	2	Α
		δ = 0.5; f = 20 kHz; $T_{sp} \le$ 140 °C; square wave		-	-	2	Α
V_R	reverse voltage	T _j = 25 °C		-	-	60	V





Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I_F = 2 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	505	575	mV
I _R	reverse current	$V_R = 60 \text{ V; } t_p \le 300 \text{ µs; } \delta \le 0.02;$ $T_j = 25 \text{ °C; pulsed}$	-	55	250	μA

^[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al_2O_3 , standard footprint.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Α	anode	3	3 - 1, 2
2	Α	anode		006aab624
3	К	cathode	Transparent top view	
			DFN2020D-3 (SOT1061D)	

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG6020EPAS	DFN2020D-3	DFN2020D-3: plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body 2 x 2 x 0.65 mm	SOT1061D		

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG6020EPAS	CQ

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	60	V
I _F	forward current	T _{sp} ≤ 135 °C; δ = 1		-	2.8	Α
I _{F(AV)}	average forward current	$\bar{\delta}$ = 0.5; f = 20 kHz; $T_{amb} \leq 65$ °C; square wave	[1]	-	2	A
		$\bar{\delta}$ = 0.5; f = 20 kHz; $T_{sp} \le$ 140 °C; square wave		-	2	A
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \ \delta \le 0.25$	[2]	-	7	Α
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave	[2]	-	18	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[3]	-	520	mW
			[4]	-	1050	mW
			[1]	-	1900	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.
- [2] Both anode pins connected.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j a)	thermal resistance	in free air	[1][2]	-	-	240	K/W
	from junction to		[1][3]	-	-	120	K/W
	ambient		[1][4]	-	-	65	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		<u>[5]</u>	-	-	10	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- 2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [5] Soldering point of cathode tab.

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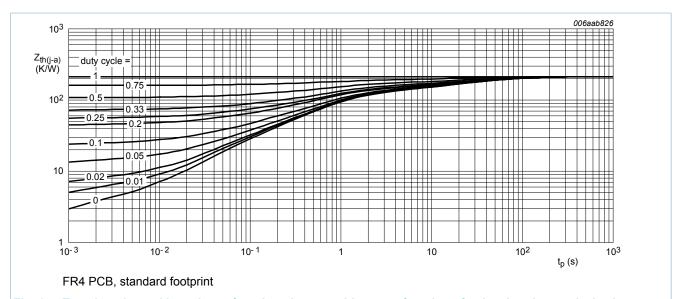


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

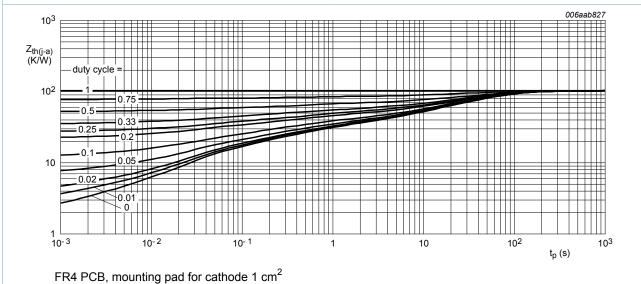
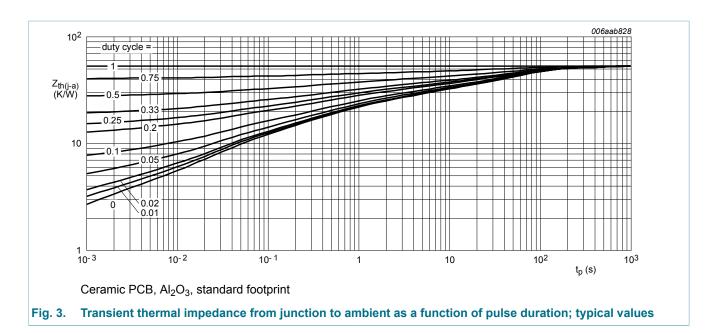


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I_R = 2 mA; t_p = 300 µs; δ = 0.02; T_j = 25 °C; pulsed	60	-	-	V
V _F	forward voltage	I_F = 0.5 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	370	-	mV
		I_F = 1 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	420	-	mV
		I_F = 2 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	505	575	mV
I _R	reverse current	$V_R = 10 \text{ V}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}; \text{ pulsed}$	-	5	-	μA
		$V_R = 60 \text{ V}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	-	55	250	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	250	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	90	-	pF
t _{rr}	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$	-	5.5	-	ns

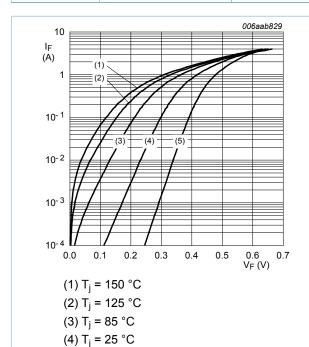
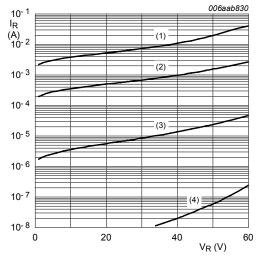


Fig. 4. Forward current as a function of forward voltage; typical values

(5) $T_i = -40 \, ^{\circ}C$



- (1) $T_i = 125 \,^{\circ}C$
- (2) $T_j = 85 \, ^{\circ}C$
- (3) $T_i = 25 \, ^{\circ}C$
- (4) $T_i = -40 \, ^{\circ}C$

Fig. 5. Reverse current as a function of reverse voltage; typical values

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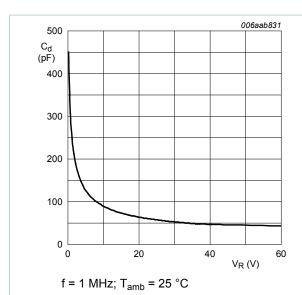
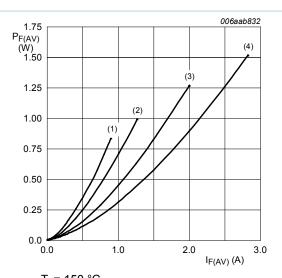
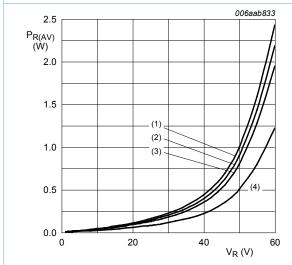


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



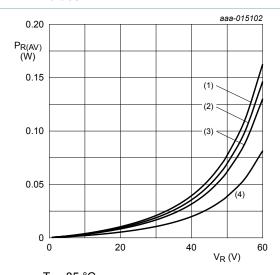
 $T_j = 150 \text{ °C}$ (1) $\delta = 0.1$ (2) $\delta = 0.2$ (3) $\delta = 0.5$ (4) $\delta = 1$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



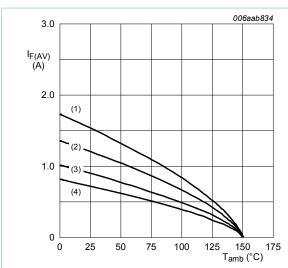
 $T_j = 125 \,^{\circ}C$ (1) $\delta = 1$ (2) $\delta = 0.9$ (3) $\delta = 0.8$ (4) $\delta = 0.5$

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



 $T_j = 85 \,^{\circ}\text{C}$ (1) $\delta = 1$ (2) $\delta = 0.9$ (3) $\delta = 0.8$ (4) $\delta = 0.5$

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T_i = 150 °C

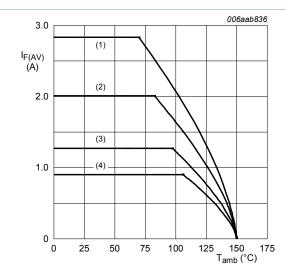
(1) δ = 1; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) δ = 0.1; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 150 °C

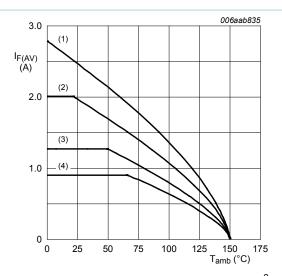
(1) δ = 1; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) δ = 0.1; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

T_i = 150 °C

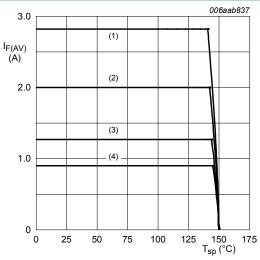
(1) δ = 1; DC

(2) δ = 0.5; f = 20 kHz

(3) δ = 0.2; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



T_i = 150 °C

(1) δ = 1; DC

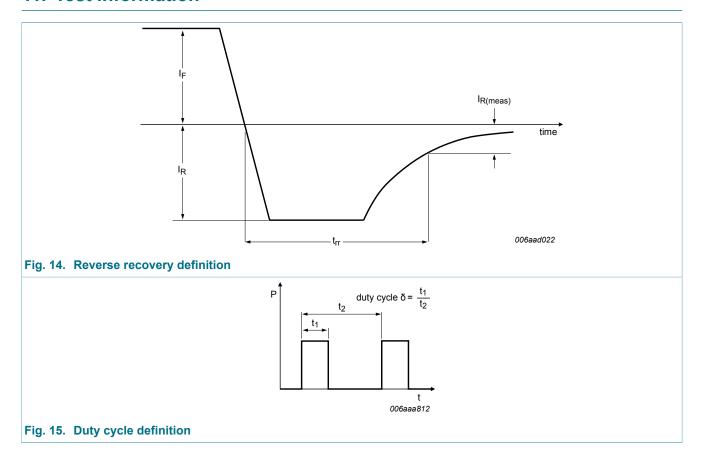
(2) δ = 0.5; f = 20 kHz

(3) δ = 0.2; f = 20 kHz

(4) δ = 0.1; f = 20 kHz

Fig. 13. Average forward current as a function of solder point temperature; typical values

11. Test information

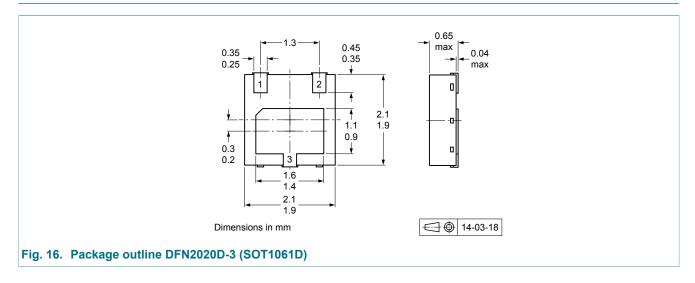


The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

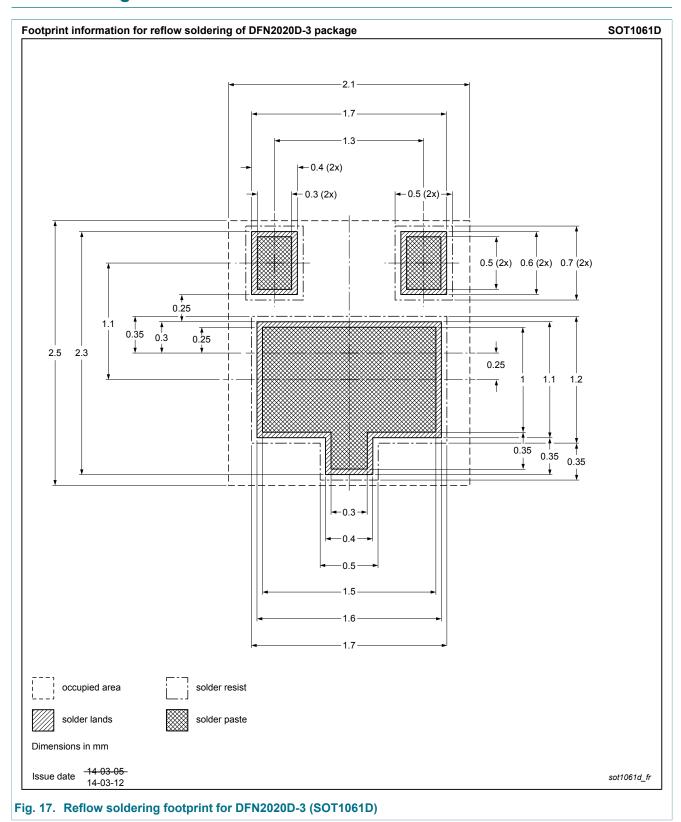
11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG6020EPAS v.2	20150119	Product data sheet	-	PMEG6020EPAS v.1		
Modification:	Product status changed					
PMEG6020EPAS v.1	20141208	Preliminary data sheet	-	-		

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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