



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = 25°C
	12mΩ @ V _{GS} = 10V	10A
30V	16mΩ @ V _{GS} = 4.5V	8.5A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Management Application
- Power Management Functions
- DC-DC Converters

Features and Benefits

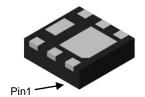
- 0.6mm profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

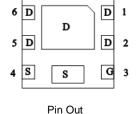
Mechanical Data

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

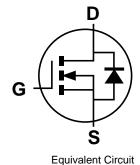
U-DFN2020-6 (Type F)







Bottom View



Top View

Bottom View

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMN3016LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel		
DMN3016LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



NZ = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016		2017	2018		2019	2020		2021	2022		2023
Code	D		Е	F		G	Н		I	J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	30	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Ocalisador Dario Ocarrel Aldre OVV		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	10 8	А
Continuous Drain Current (Note 6) V _{GS} = 10V	$t < 10s$ $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		I _D	12 9	А
Maximum Continuous Body Diode Forward Current	t (Note 6)	I _S	2.5	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%	6)	I _{DM}	50	Α	
Avalanche Current (Note 7) L = 0.1mH		I _{AR}	22	Α	
Avalanche Energy (Note 7) L = 0.1mH		E _{AR}	24	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.73	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.47		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Ъ	174	°C/W	
memial Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	121		
Total Power Dissipation (Note 6)	$T_A = +25$ °C	PD	2.02	W	
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	FD	1.30	۷V	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	В	66	°C/W	
Themal Resistance, Junction to Ambient (Note o)	t<10s	$R_{\theta JA}$	42		
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{ heta JC}$	11.6		
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	-				•		
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μΑ	$V_{DS} = 30V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	-	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Р	-	8	12	mΩ	$V_{GS} = 10V, I_D = 11A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	-	12	16	11122	$V_{GS} = 4.5V, I_D = 9A$	
Diode Forward Voltage	V _{SD}	-	0.70	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)						•	
Input Capacitance	Ciss	-	1415	-		\\\\ 45\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
Output Capacitance	Coss		119	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	1	82	-		1 = 1.01/11 12	
Gate Resistance	Rg	-	2.6	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_g	-	11.3	-			
Total Gate Charge (V _{GS} = 10V)	Qg	-	25.1	-	nC	., .=,,,	
Gate-Source Charge	Q _{gs}	-	3.5	-	IIC	$V_{DS} = 15V, I_{D} = 12A$	
Gate-Drain Charge	Q_{gd}	-	3.6	-			
Turn-On Delay Time	t _{D(ON)}	-	4.8	-			
Turn-On Rise Time	t _R	-	16.5	-		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	26.1	-	ns	$R_L = 1.25\Omega$, $R_g = 3\Omega$	
Turn-Off Fall Time	t _F	-	5.6	-		-	
Reverse Recovery Time	t _{RR}	-	12.3	-	ns	1 404 11/11 5004/	
Reverse Recovery Charge	Q _{RR}	-	10.4	-	nC	$I_F = 12A$, di/dt = 500A/ μ s	

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



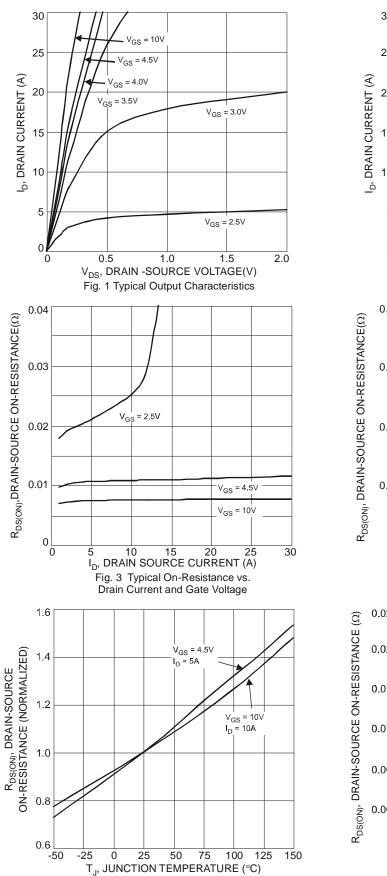
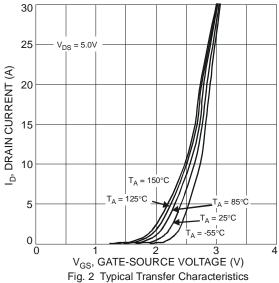
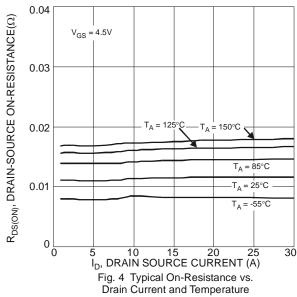
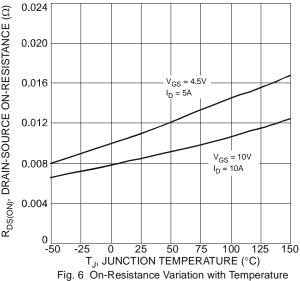


Fig. 5 On-Resistance Variation with Temperature









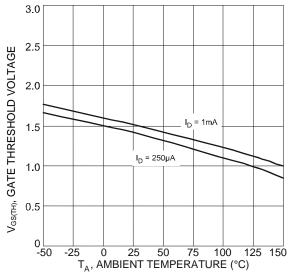
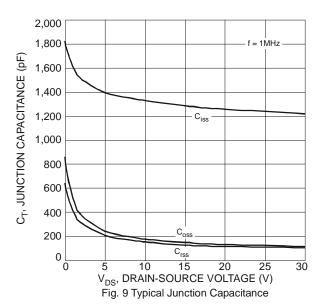
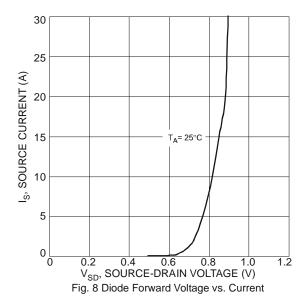
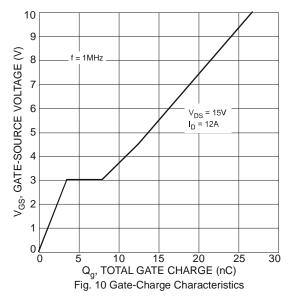


Fig. 7 Gate Threshold Variation vs. Ambient Temperature









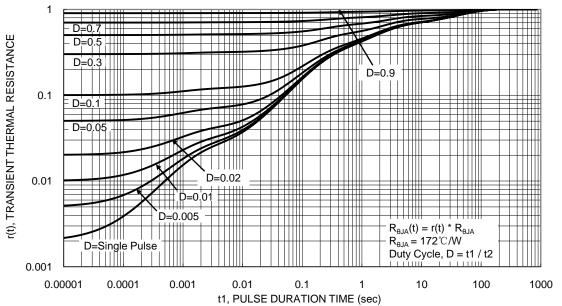


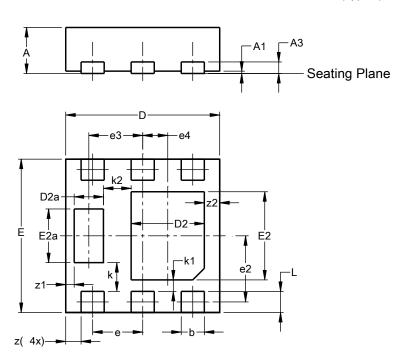
Figure 11. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

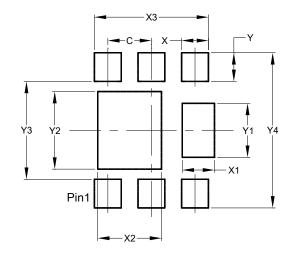


U-DFN2020-6									
(Type F)									
Dim	Min Max Typ								
Α	0.57	0.63	0.60						
A1	0.00	0.05	0.03						
A3	0.15								
b	0.25	0.35	0.30						
D	1.95	2.05	2.00						
D2	0.85	1.05	0.95						
D2a	0.33	0.43	0.38						
E	1.95 2.05 2.00								
E2	1.05 1.25 1.15								
E2a	0.65 0.75 0.70								
е	0.65 BSC								
e2	0.863 BSC								
е3		0.70 BS	С						
e4	C).325 BS	SC SC						
k		0.37 BS							
k1	0.15 BSC								
k2	0.36 BSC								
L	0.225 0.325 0.275								
Z	0.20 BSC								
z1	().110 BS	SC						
z2	0.20 BSC								
All Dimensions in mm									

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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