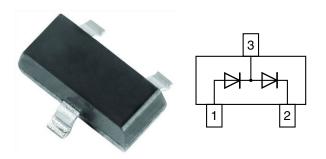


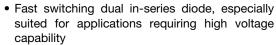
Vishay Semiconductors

Dual In-Series Small Signal High Voltage Switching Diode



FEATURES

• Silicon epitaxial planar diode





AEC-Q101 qualified

• Base P/N-G3 - green, commercial grade

ROHS COMPLIANT HALOGEN FREE

Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912

GREEN (5-2008)

MECHANICAL DATA

Case: SOT-23

Weight: approx. 8.1 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE						
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS		
GSD2004S-G	GSD2004S-G3-08 or GSD2004S-G3-18	Dual diodes serial	DB7	Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		V_R	240	V	
Peak repetitive reverse voltage		V_{RRM}	300	V	
Forward current (continuous)		I _F	225	mA	
Peak repetitive forward current		I _{FRM}	625	mA	
Non repetitive peak ferward aurrent	t _p = 1 μs	I _{FSM}	4.0	А	
Non-repetitive peak forward current	t _p = 1 s	I _{FSM}	1.0	А	
Power dissipation (1)		P _{tot}	350	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Typical thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	357	°C/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	- 65 to + 150	°C	
Operating temperature range		T _{op}	- 55 to + 150	°C	

Note

(1) Device on fiberglass substrate



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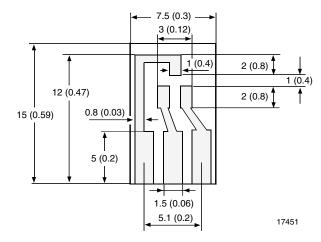
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I _R = 100 μA	V_{BR}	300			V
Lookago ourrent	V _R = 240 V	I _R			100	nA
Leakage current	$V_R = 240 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I _R			100	μA
Forward voltage	I _F = 20 mA	V _F		0.83	0.87	V
Forward voltage	I _F = 100 mA	V _F			1.00	V
Diode capacitance	$V_F = V_R = 0$, $f = 1$ MHz	C _D			5.0	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $i_R = 3.0$ mA, $R_L = 100~\Omega$	t _{rr}			50	ns

Note

LAYOUT FOR RthJA TEST

Thickness:

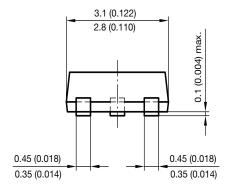
Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)

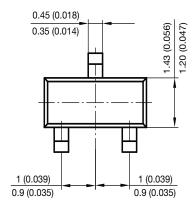


⁽¹⁾ Device on fiberglass substrate

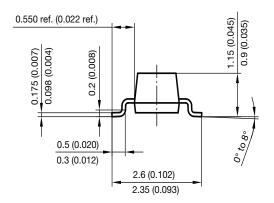
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PACKAGE DIMENSIONS in millimeters (inches): SOT-23

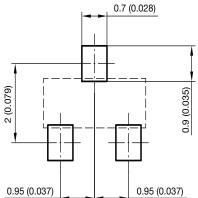




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Foot print recommendation:





Legal Disclaimer Notice

Vishay

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