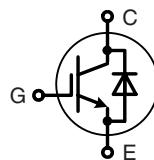


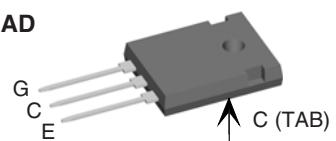
High Voltage BIMOSFET™ Monolithic Bipolar MOS Transistor

N-Channel, Enhancement Mode
MOSFET compatible



I_{C25} = 9 A
 V_{CES} = 1600 V
 $V_{CE(sat)}$ = 4.9 V typ.
 t_{fi} = 70 ns

TO-247 AD



G = Gate, C = Collector,
E = Emitter, TAB = Collector

Symbol	Conditions	Maximum Ratings			Features
V_{CES}	$T_J = 25^\circ\text{C}$ to 150°C	1600	V		• High Voltage BIMOSFET™
V_{CGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GE} = 1 \text{ M}\Omega$	1600	V		- replaces high voltage Darlingtons and series connected MOSFETs
V_{GES}	Continuous	± 20	V		- lower effective $R_{DS(on)}$
V_{GEM}	Transient	± 30	V		• MOS Gate turn-on
I_{C25}	$T_C = 25^\circ\text{C}$	9	A		- drive simplicity
I_{C90}	$T_C = 90^\circ\text{C}$	5	A		- MOSFET compatible for 10V turn on gate voltage
I_{CM}	$T_C = 25^\circ\text{C}, 1 \text{ ms}$	10	A		• Monolithic construction
SSOA (RBSOA)	$V_{GE} = 10 \text{ V}$, $T_{VJ} = 125^\circ\text{C}$, $R_G = 27 \Omega$, $V_{CE} = 0.8 \cdot V_{CES}$ Clamped inductive load, $L = 100 \mu\text{H}$	$I_{CM} = 12$	A		- high blocking voltage capability
P_c	$T_C = 25^\circ\text{C}$	100	W		- very fast turn-off characteristics
T_J		-55 ... +150	$^\circ\text{C}$		• International standard package
T_{JM}		150	$^\circ\text{C}$		JEDEC TO-247 AD
T_{stg}		-55 ... +150	$^\circ\text{C}$		• Reverse conducting capability
T_L	1.6 mm (0.063 in) from case for 10 s	300	$^\circ\text{C}$		
M_d	Mounting torque	1.15	Nm		
Weight		6	g		

Symbol	Conditions	Characteristic Values			Applications
		($T_J = 25^\circ\text{C}$, unless otherwise specified)			
		min.	typ.	max.	
BV_{CES}	$I_C = 0.25 \text{ mA}$, $V_{GE} = 0 \text{ V}$	1600		V	• Flyback converters
$V_{GE(th)}$	$I_C = 0.5 \text{ mA}$, $V_{CE} = V_{GE}$	3.5		5.5 V	• DC choppers
I_{CES}	$V_{CE} = 0.8 \cdot V_{CES}$ $V_{GE} = 0 \text{ V}$	$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	0.1	100 μA mA	• Uninterruptible power supplies (UPS)
I_{GES}	$V_{CE} = 0 \text{ V}$, $V_{GE} = \pm 20 \text{ V}$			$\pm 500 \text{ nA}$	• Switched-mode and resonant-mode power supplies
$V_{CE(sat)}$	$I_C = I_{C90}$, $V_{GE} = 15 \text{ V}$	$T_J = 125^\circ\text{C}$	4.9 5.6	7 V V	• CRT deflection

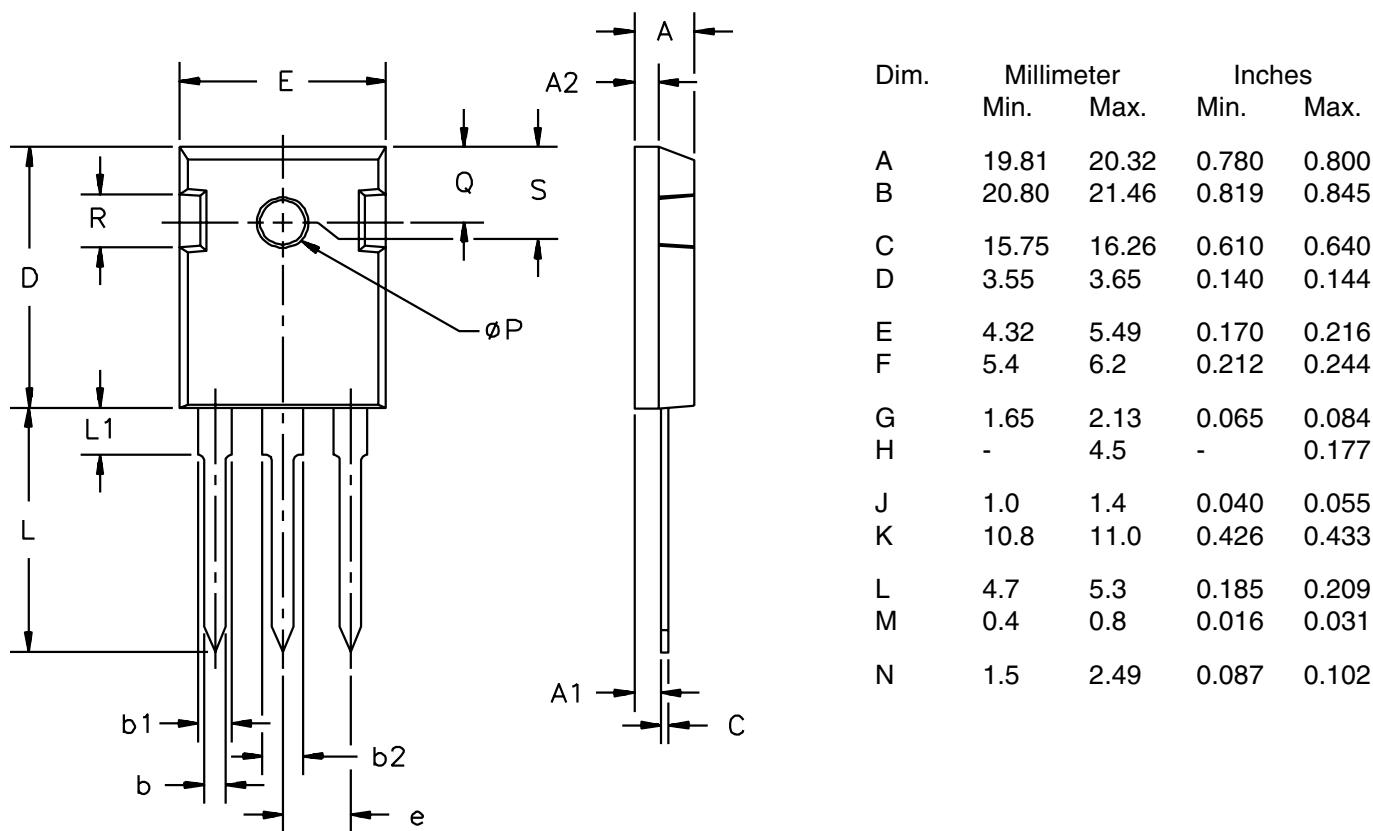
Symbol	Conditions	Characteristic Values			
		($T_J = 25^\circ\text{C}$, unless otherwise specified)	min.	typ.	max.
C_{ies}	$V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	550		pF	
C_{oes}		36		pF	
C_{res}		5		pF	
Q_g	$I_C = 5 \text{ A}, V_{CE} = 600 \text{ V}, V_{GE} = 10 \text{ V}$	34		nC	
$t_{d(on)}$	Inductive load, $T_J = 125^\circ\text{C}$	140		ns	
t_{ri}		200		ns	
$t_{d(off)}$		120		ns	
t_{fi}		70		ns	
R_{thJC}			0.25	1.25	K/W
R_{thCK}					K/W

Reverse Conduction

Characteristic Values
($T_J = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Conditions	min.	typ.	max.
V_F	$I_F = I_{C90}, V_{GE} = 0 \text{ V}$	3.6	5	

TO-247 AD Outline



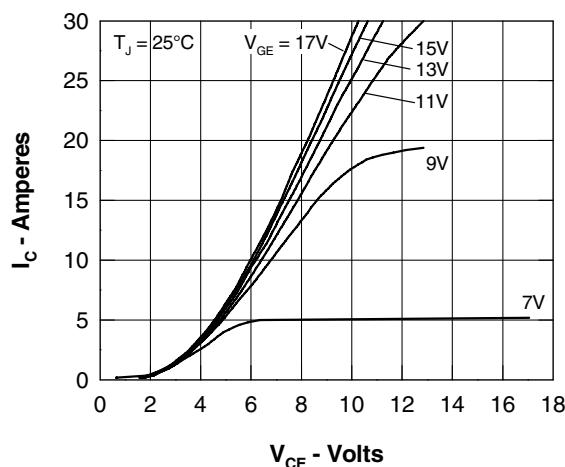


Fig. 1 Typ. Output Characteristics

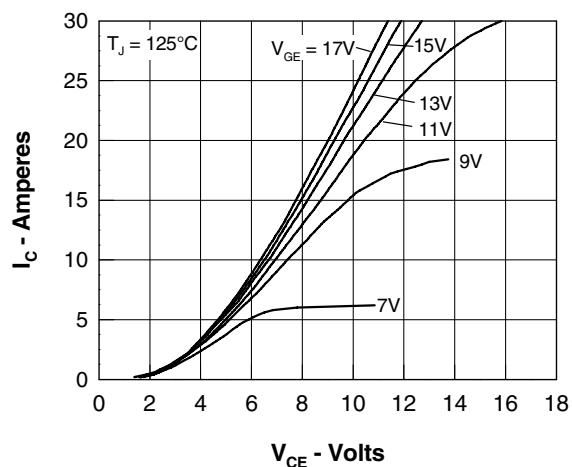


Fig. 2 Typ. Output Characteristics

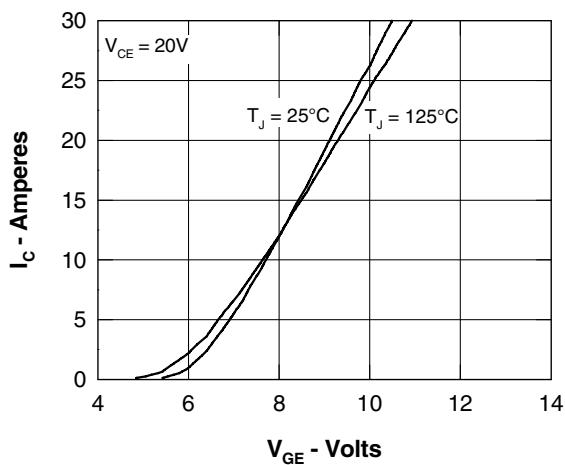


Fig. 3 Typ. Transfer Characteristics

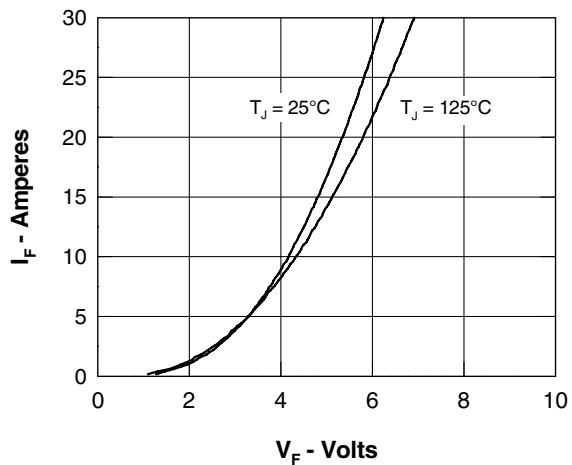


Fig. 4 Typ. Characteristics of Reverse Conduction

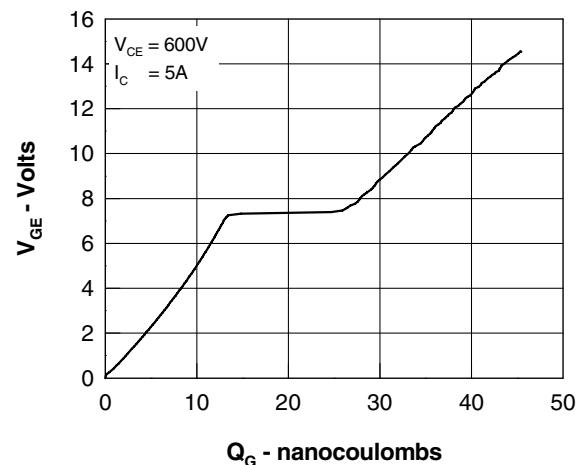


Fig. 5 Typ. Gate Charge characteristics

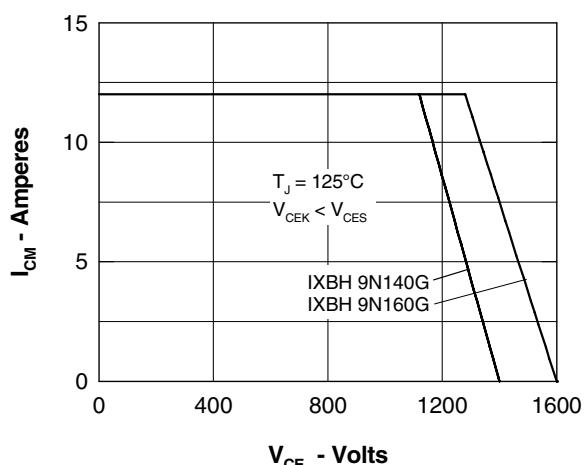


Fig. 6 Reverse Biased Safe Operating Area RBSOA

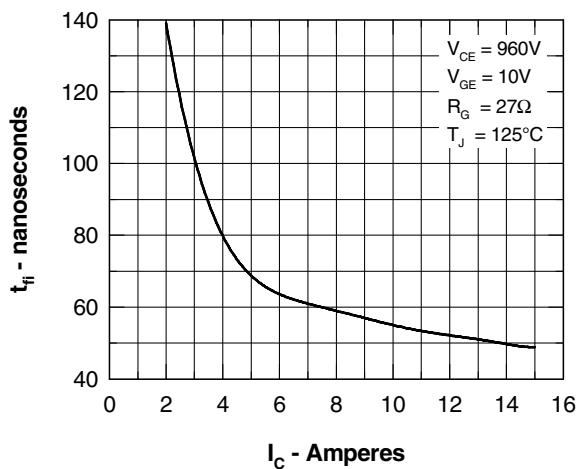


Fig. 7 Typ. Fall Time

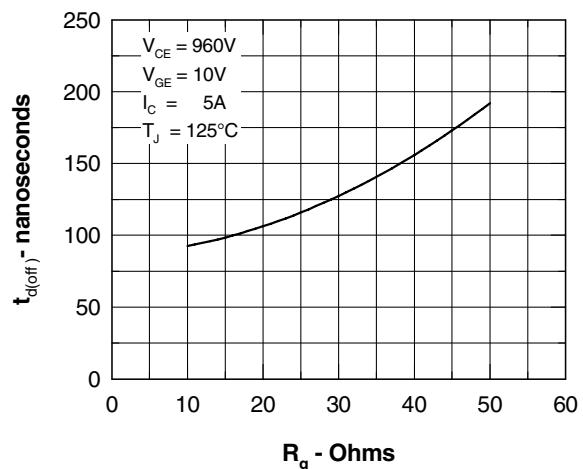


Fig. 8 Typ. Turn Off Delay Time

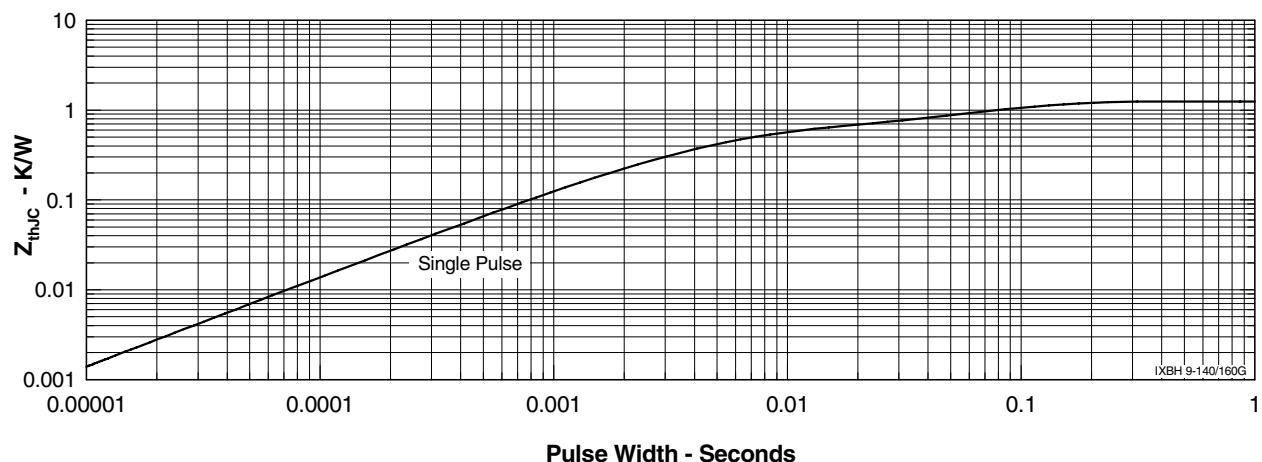


Fig. 9 Typ. Transient Thermal Impedance