

#### Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <a href="http://www.nxp.com">http://www.nxp.com</a>, <a href="http://www.semiconductors.philips.com/">http://www.nxp.com</a>, <a href="http://www.nexperia.com">http://www.nexperia.com</a>, <a href="http://www.nexperia.com">http://www.nexperia.com</a>,

Instead of sales.addresses@www.nxp.com or sales.addresses@www.semiconductors.philips.com, use salesaddresses@nexperia.com (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

Should be replaced with:

- © Nexperia B.V. (year). All rights reserved.

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

**Product data sheet** 

# 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Enhanced power dissipation capability of 1200 mW
- ElectroStatic Discharge (ESD) protection: 2 kV HBM

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
V <sub>GS</sub>	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	7.2	Α
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 4.5 V; $I_D$ = 5.7 A; $T_j$ = 25 °C		-	19	23	mΩ

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.





30 V, N-channel Trench MOSFET

# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	<u></u> 3	D I
2	S	source		
3	D	drain	TO-236AB (SOT23)	G S 017aaa255

# 6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMV20XNE	TO-236AB	plastic surface-mounted package; 3 leads	SOT23		

# 7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PMV20XNE	%G9

[1] % = placeholder for manufacturing site code

30 V, N-channel Trench MOSFET

# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
$V_{GS}$	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	$V_{GS} = 4.5 \text{ V}; T_{amb} = 25 \text{ °C}; t \le 5 \text{ s}$	[1]	-	7.2	Α
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	5.7	Α
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	3.6	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	24	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	510	mW
			[1]	-	1200	mW
		T <sub>sp</sub> = 25 °C		-	6940	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode	,	ı			
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.2	Α

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

<sup>[2]</sup> Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

#### 30 V, N-channel Trench MOSFET

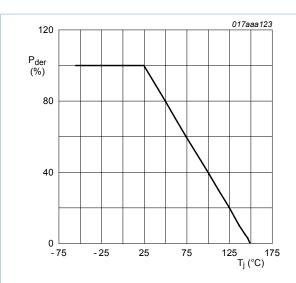


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

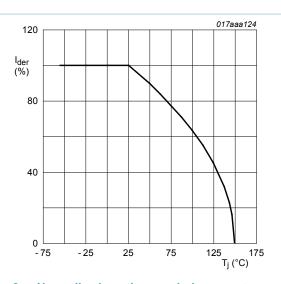


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

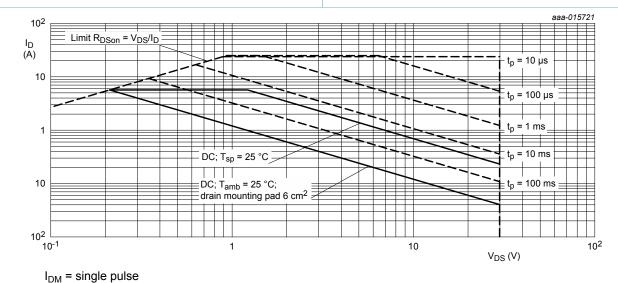


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

### 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance	in free air	[1]	-	208	245	K/W
	from junction to ambient		<u>[2]</u>	-	88	104	K/W
	ambient	in free air; t ≤ 5 s	[2]	-	55	65	K/W

PMV20XNE

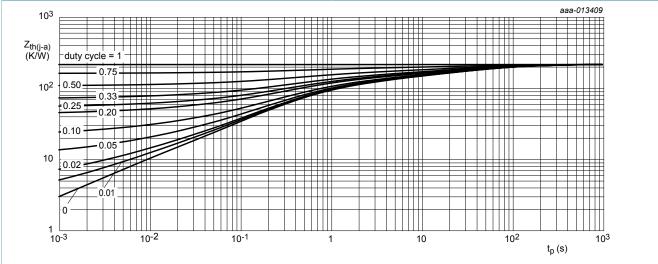
All information provided in this document is subject to legal disclaimers.

© NXP Semiconductors N.V. 2014. All rights reserved

### 30 V, N-channel Trench MOSFET

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	13	18	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

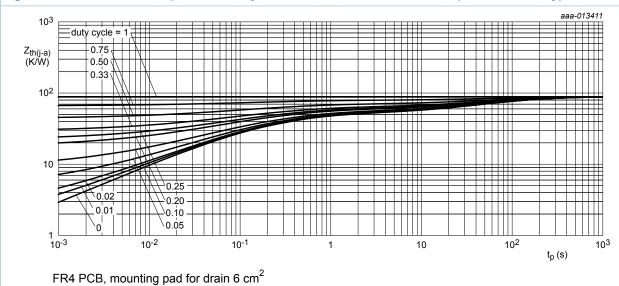


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

30 V, N-channel Trench MOSFET

### 10. Characteristics

### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.4	0.65	0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
	3	V <sub>GS</sub> = -8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-10	μA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 5.7 A; T <sub>j</sub> = 25 °C	-	19	23	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 5.7 A; T <sub>j</sub> = 150 °C	-	31	37	mΩ
		$V_{GS}$ = 2.5 V; $I_D$ = 5 A; $T_j$ = 25 °C	-	22	30	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 1.9 A; T <sub>j</sub> = 25 °C	-	27	38	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 2 A; $T_{j}$ = 25 °C	-	11	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C	-	1.8	-	Ω
Dynamic ch	aracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 10 V; $I_{D}$ = 5 A; $V_{GS}$ = 4.5 V;	-	12.4	18.6	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C	-	1.2	-	nC
$Q_{GD}$	gate-drain charge		-	2.1	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	1150	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	110	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	85	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 5 A; V <sub>GS</sub> = 4.5 V;	-	8	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega$ ; $T_j = 25 °C$	-	17	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	33	-	ns
t <sub>f</sub>	fall time		-	32	-	ns
Source-drai	in diode		<u> </u>		1	
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 1.2 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.7	1.2	V

6 / 15

### 30 V, N-channel Trench MOSFET

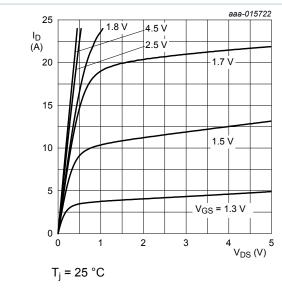


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

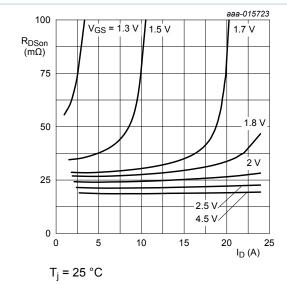


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

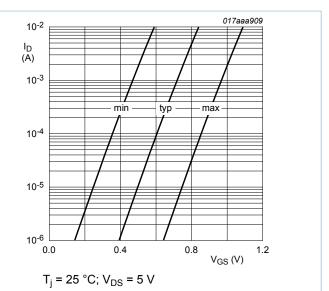


Fig. 7. Subthreshold drain current as a function of gate-source voltage

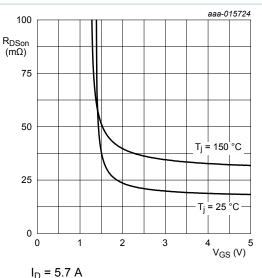


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

### 30 V, N-channel Trench MOSFET

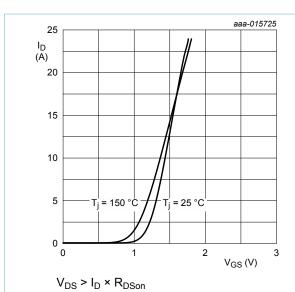


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

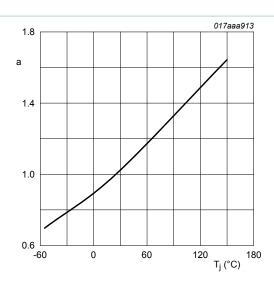


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

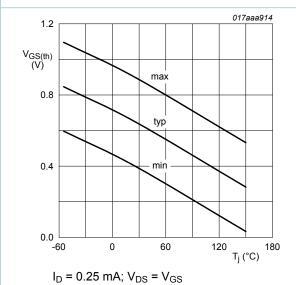
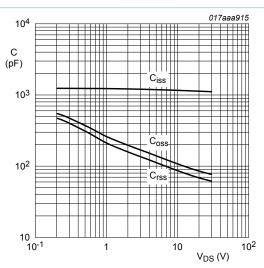


Fig. 12. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$ 

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

30 V, N-channel Trench MOSFET

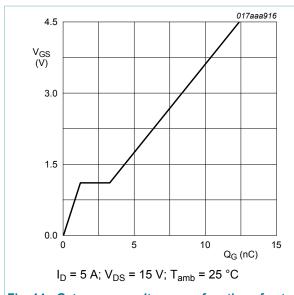


Fig. 14. Gate-source voltage as a function of gate charge; typical values

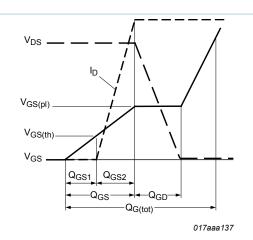


Fig. 15. MOSFET transistor: Gate charge waveform definitions

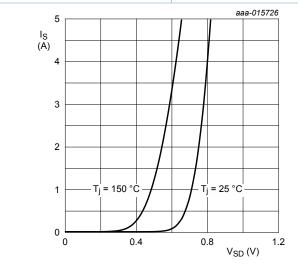
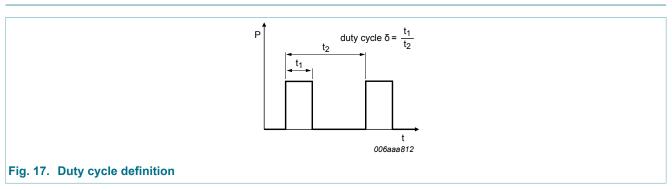


Fig. 16. Source current as a function of source-drain voltage; typical values

### 11. Test information

 $V_{GS} = 0 V$ 



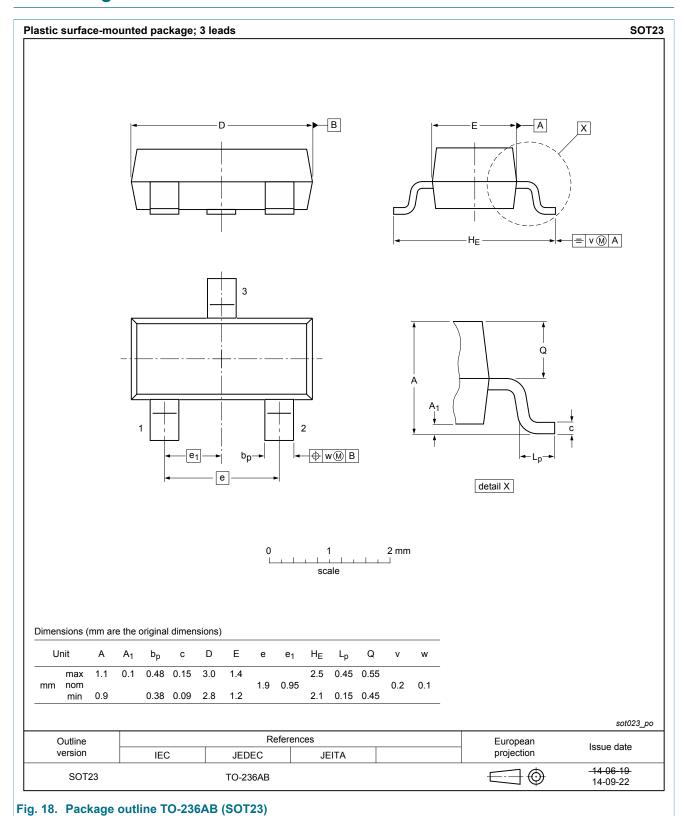
PMV20XNE

All information provided in this document is subject to legal disclaimers.

© NXP Semiconductors N.V. 2014. All rights reserved

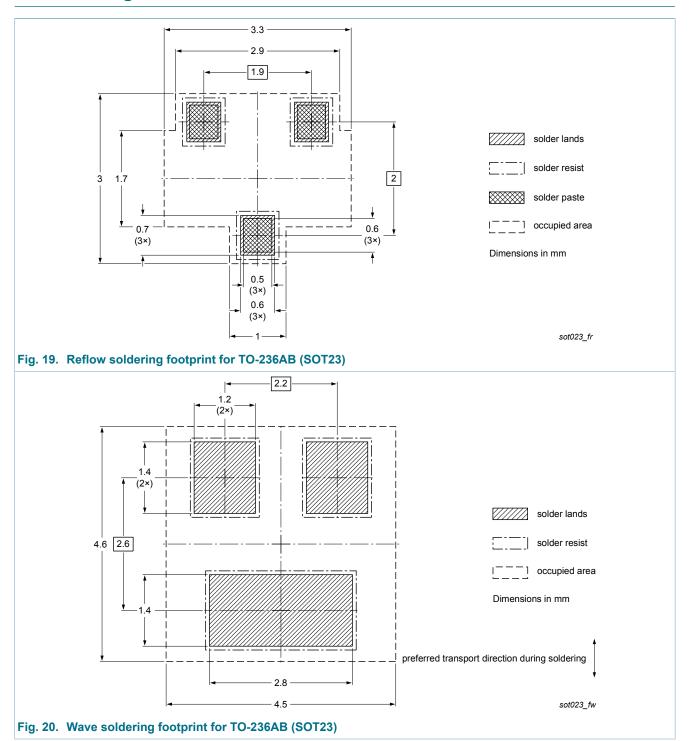
30 V, N-channel Trench MOSFET

# 12. Package outline



30 V, N-channel Trench MOSFET

### 13. Soldering



30 V, N-channel Trench MOSFET

# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMV20XNE v.1	20141110	Product data sheet	-	-

#### 30 V, N-channel Trench MOSFET

### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

#### 15.2 Definitions

**Preview** — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the

PMV20XNE

All information provided in this document is subject to legal disclaimers.

© NXP Semiconductors N.V. 2014. All rights reserved

30 V, N-channel Trench MOSFET

grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### 15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Bitsound, CoolFlux, CoReUse, DESFire, FabKey, GreenChip, HiPerSmart, HITAG, I<sup>2</sup>C-bus logo, ICODE, I-CODE, ITEC, MIFARE, MIFARE Plus, MIFARE Ultralight, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE — are trademarks of NXP Semiconductors N.V.

 ${\bf HD}$   ${\bf Radio}$  and  ${\bf HD}$   ${\bf Radio}$   ${\bf logo}$  — are trademarks of iBiquity Digital Corporation.

### 30 V, N-channel Trench MOSFET

### 16. Contents

2 Features and benefits 3 Applications 4 Quick reference data 5 Pinning information 6 Ordering information 7 Marking 8 Limiting values 9 Thermal characteristics 10 Characteristics 11 Test information 12 Package outline 13 Soldering 14 Revision history 15 Legal information 15.1 Data sheet status 15.2 Definitions 15.3 Disclaimers 15.4 Trademarks	1	General description	1
4 Quick reference data	2	Features and benefits	1
5 Pinning information	3	Applications	1
6 Ordering information	4	Quick reference data	1
7 Marking	5	Pinning information	2
8 Limiting values 9 Thermal characteristics 10 Characteristics 11 Test information 12 Package outline 13 Soldering 14 Revision history 15 Legal information 15.1 Data sheet status 15.2 Definitions 15.3 Disclaimers	6	Ordering information	2
9 Thermal characteristics	7	Marking	2
10 Characteristics	8	Limiting values	3
11 Test information	9	Thermal characteristics	4
12 Package outline  13 Soldering	10	Characteristics	6
13 Soldering	11	Test information	9
14 Revision history	12	Package outline	10
15Legal information15.1Data sheet status15.2Definitions15.3Disclaimers	13	Soldering	11
15.1 Data sheet status	14	Revision history	12
15.2 Definitions	15	Legal information	13
15.3 Disclaimers	15.1	Data sheet status	13
	15.2	Definitions	13
15.4 Trademarks	15.3	Disclaimers	13
	15.4	Trademarks	14

### © NXP Semiconductors N.V. 2014. All rights reserved

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 10 November 2014